

REMARKS

Claims 45-65 are pending in this application. By this Amendment, claims 22-31 and 33-44 are canceled without prejudice to, or disclaimer of, the subject matter therein. New claims 45-65 are added. Reconsideration based on the above amendments and following remarks is respectfully requested.

The Office Action rejects claims 22-24, 27-29 and 36-44 under 35 U.S.C. §103(a) over U.S. Patent No. 5,412,397 to Kanatani et al. (hereafter "Kanatani") in view of U.S. Patent No. 5,576,730 to Shimada et al. (hereafter "Shimada") and U.S. Patent No. 5,481,651 to Herold; and rejects claims 25, 26, 30 and 33 under 35 U.S.C. §103(a) over Kanatani in view of Shimada and further in view of U.S. Patent No. 4,816,816 to Usui (hereafter "Usui"). The rejections are respectfully traversed.

Claims 22-31 and 33-44 are canceled without prejudice to, or disclaimer of, the subject matter therein. Therefore, it is respectfully requested that the rejections of those claims be withdrawn.

New claims 45-65 are patentable over the references applied in rejecting the canceled claims for at least the following reasons.

The subject matter recited in the claims has a first driving circuit and a second driving circuit. Claim 45 recites "a first data line driving circuit inputting a plurality of analog signals to the plurality of pixel transistors via the plurality of data lines and a second data line driving circuit providing a plurality of digital signals to the plurality of data lines." Claim 54 recites "the first data line driving circuit having a different function from a function of the second data line driving circuit." Claim 64 includes similar recitations.

Kanatani discloses a first driving circuit and a second driving circuit, the first driving circuit inputting a plurality of first signals to a plurality of data lines and the second driving

circuit inputting a plurality of second signals to the plurality of data lines. But the first driving circuit is composed of the same structure as the second driving circuit.

Shimada discloses only a first driving circuit (disclosed in its specification as "source driving circuit") and there is no second driving circuit.

Herold discloses a plurality of column drivers 504 that are connecting to an LCD display panel 100 from an upper side and a lower side. But the column drivers at the upper side are the same drivers as the column drivers at the lower side.

Usui fails to overcome these deficiencies in Kamatani, Shimada and Herold.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 45-65 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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